

PATENT  
Docket No. CA7016672001

CERTIFICATE OF MAILING (37 C.F.R. § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date indicated below with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: December 5, 2003

Signature of Person Certifying: Steve Elliott  
Printed Name: Steve Elliott

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Louis K. Scheffer

Serial No.: 10/660,986

Filed: September 12, 2003

For: AUTOMATIC INSERTION OF  
CLOCKED ELEMENTS INTO AN  
ELECTRONIC DESIGN TO IMPROVE  
SYSTEM PERFORMANCE

)  
) Group Art Unit: Not yet assigned  
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) Examiner: Not yet assigned  
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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 CFR §§ 1.97 and 1.98, the items identified in this Information Disclosure Statement ("IDS") are brought to the attention of the Office. The items are listed on the attached form(s) PTO/SB/08b. Copies of the items listed are enclosed herewith.

The items identified in this IDS may or may not be "material" pursuant to 37 CFR § 1.56. The submission thereof by Applicants is not to be construed as an admission that any such patent, publication or other information referred to therein is material or considered to be material (37 CFR § 1.97(h)), or even qualifies as "prior art" under 35 USC § 102 with respect to this invention unless specifically designated by Applicants as such.

INFORMATION DISCLOSURE STATEMENT FILING PROVISION:

☒ This IDS is believed to be timely in that it is being submitted under 37 CFR § 1.97(b), that is (1) within three months of the filing date of the application, which is not a continued prosecution application filed under § 1.53(d) or (2) within three months of entry of the national stage as set forth in 37 CFR § 1.491; or (3) before the mailing of a first Office action on the merits; or (4) before the mailing of a first Office action after filing a request for continued examination under § 1.114. Thus, no fee is required.

☒ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and charge the fee due under 37 CFR § 1.17(p) to the deposit account referenced below.

☐ However, if the undersigned is in error in this regard, Applicant respectfully requests that the Office consider this IDS as filed under 37 CFR § 1.97(c), if applicable, and a statement under 37 CFR § 1.97(e) is included below, thus no fee is required.

☐ This IDS is being submitted under 37 CFR § 1.97(c), that is after mailing of a first Office Action on the merits, but before a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311.

☐ The fee due under 37 CFR § 1.17(p) is submitted herewith.

☐ A statement under 37 CFR § 1.97(e) is included below, thus no fee is required. In the event that this IDS is not received before a Final Action or a Notice of Allowance, then Applicant respectfully requests that the Office consider the filing of these papers to be submitted under 37 CFR § 1.97(d) and charge the fee due under 37 CFR § 1.17(p) to the deposit account below.

☐ This IDS is being submitted under 37 CFR § 1.97(d), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee. A statement under 37 CFR § 1.97(e) is included below. The fee due under 37 CFR § 1.17(p) is submitted herewith.

☐ This IDS is being submitted under 37 CFR § 1.97(i), that is after a Final Action under 37 CFR § 1.113 or a Notice of Allowance under 37 CFR § 1.311, but before payment of the issue fee.

**STATEMENT UNDER 37 CFR § 1.97(e):**

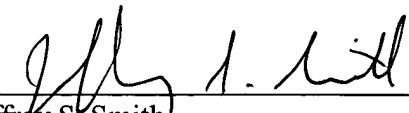
- ☐ Each item contained in this IDS was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS.
- ☐ No item contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing this statement after making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this IDS.

**PAYMENT AND/OR AUTHORIZATION TO CHARGE FEES:**

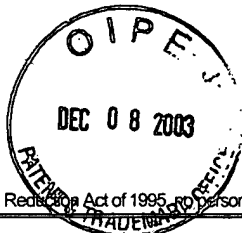
- ☐ A check in the amount of \_\_\_\_\_ is enclosed for the above fee(s).
- ☒ The Commissioner is authorized to charge the fee due under 37 CFR § 1.17(p) to the Bingham McCutchen's Deposit Account No. **50-2518, referencing billing number CA7016672001**. The Commissioner is authorized to credit any overpayment or to charge any underpayment to Bingham McCutchen's Deposit Account No. **50-2518, referencing billing number CA7016672001**.

Respectfully submitted,

Dated: December 5, 2003

By:   
Jeffrey S. Smith  
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PTO/SB/08b(08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

**Complete if Known**

Application Number	10/660,986
Filing Date	September 12, 2003
First Named Inventor	Louis K. Scheffer
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	CA7016672001

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1	"International Technology Roadmap for Semiconductors", 2001	
	2	McINERNEY, RORY et al., "Methodology for Repeater Insertion Management in the RTL, Layout, Floorplan and Fullchip Timing Databases of Itanium Microprocessor", International Symposium on Physical Design, 2000, pp. 99-104.	
	3	CHINNERY, D.G. et al., "Closing the Gap between ASIC and Custom: An ASIC Perspective", Proceedings of the Design Automation Conference, June 2000, pp. 637-342.	
	4	CARLONI, LUCA P. et al., "Theory of Latency-Insensitive Design", IEEE Transactions in Computer-Aided Design of Integrated Circuits and Systems, Vol. 20, No. 9, September 2001, pp. 1059-1076.	
	5	CARLONI, LUCA P. et al., "Latency Insensitive Protocols", Computer Aided Verification, 11 <sup>th</sup> International Conference, CAV'99, Trento, Italy, July 1999, pp123-133.	
	6	"CARLONI, LUCA P. et al., "A Methodology for Correct-by-Construction Latency Insensitive Design", Proc. ICCAD, San, Jose, CA, November 1999, pp. 309-315.	
	7	LU, RUIBING et al., "Flip-Flop and Repeater Insertion for Early Interconnect Planning", Proceedings of the 2002 Design, Automation and Test in Europe Conference, 2002.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.